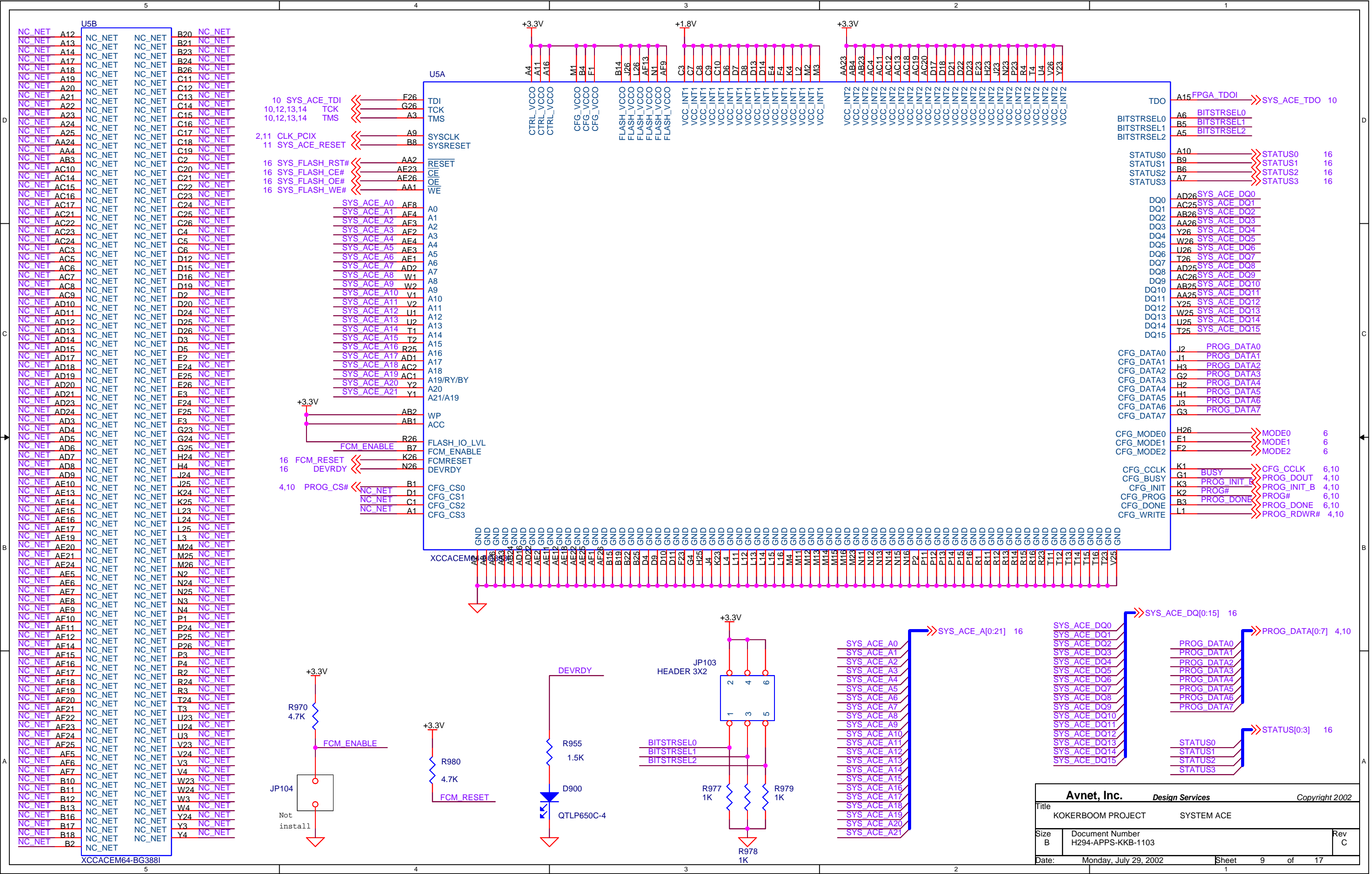
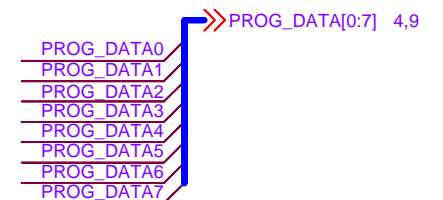
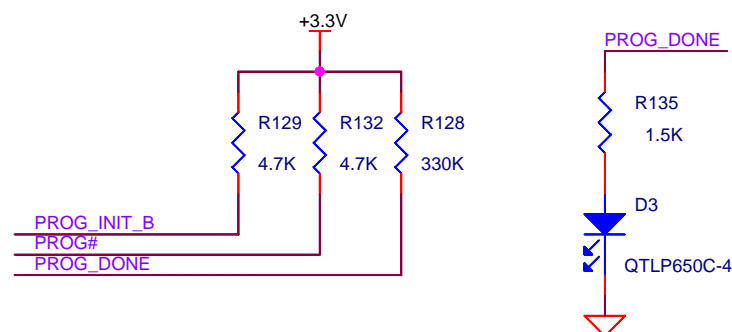
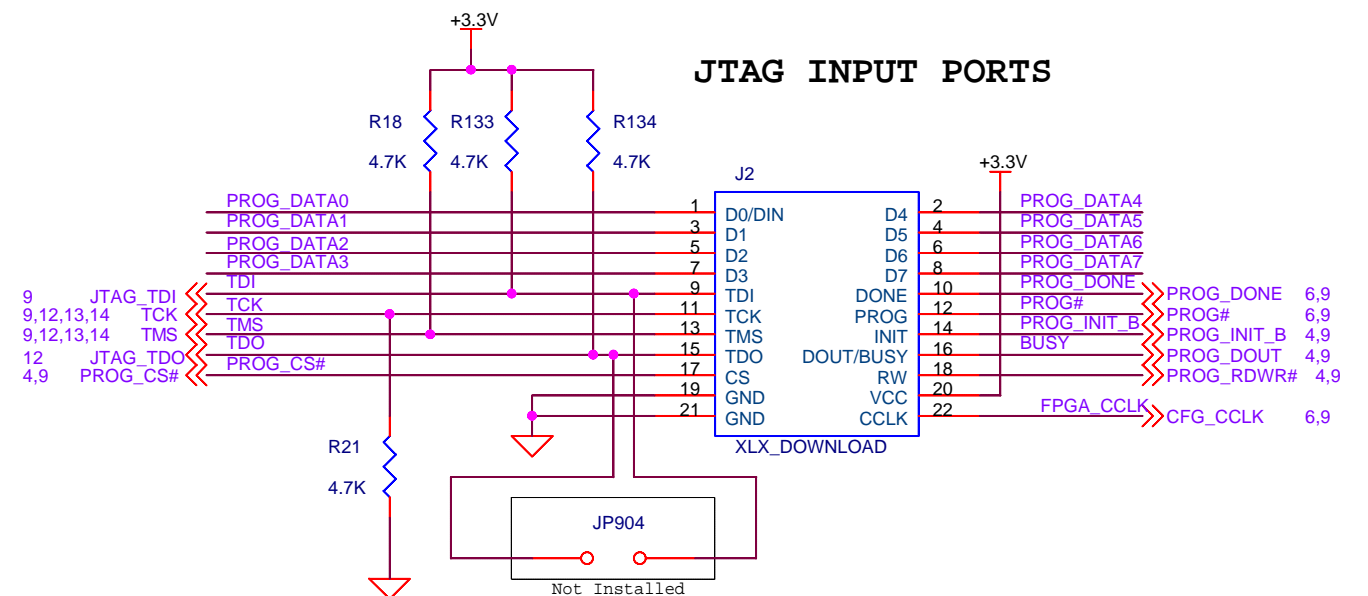
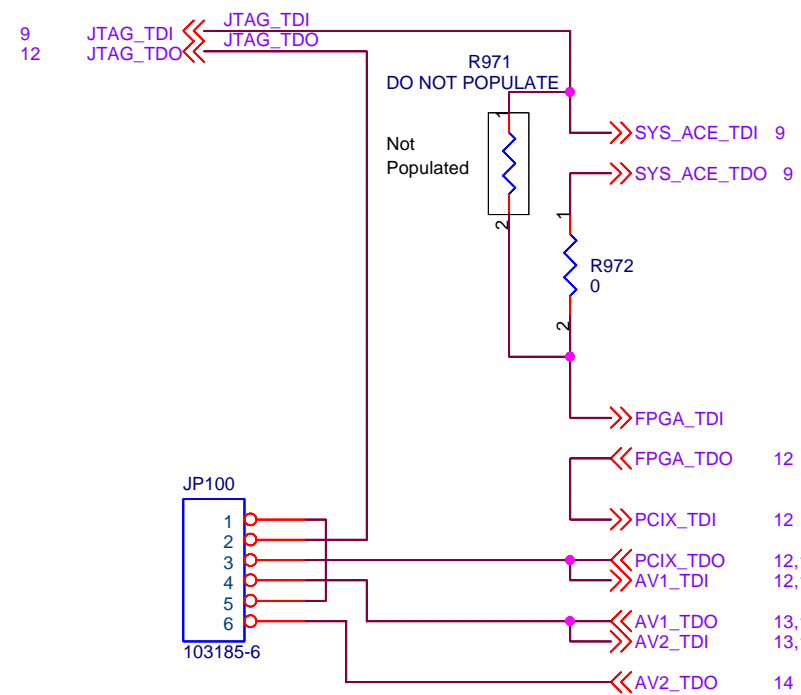
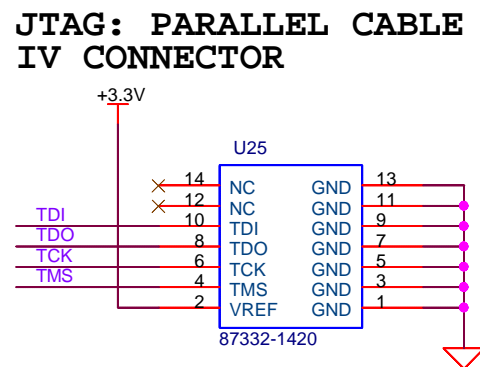
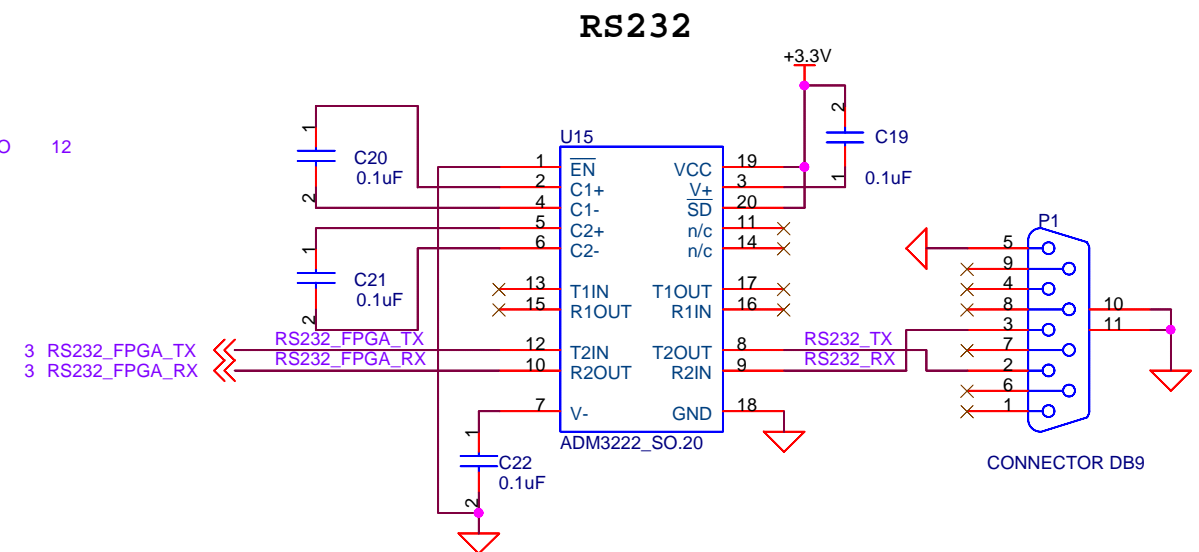


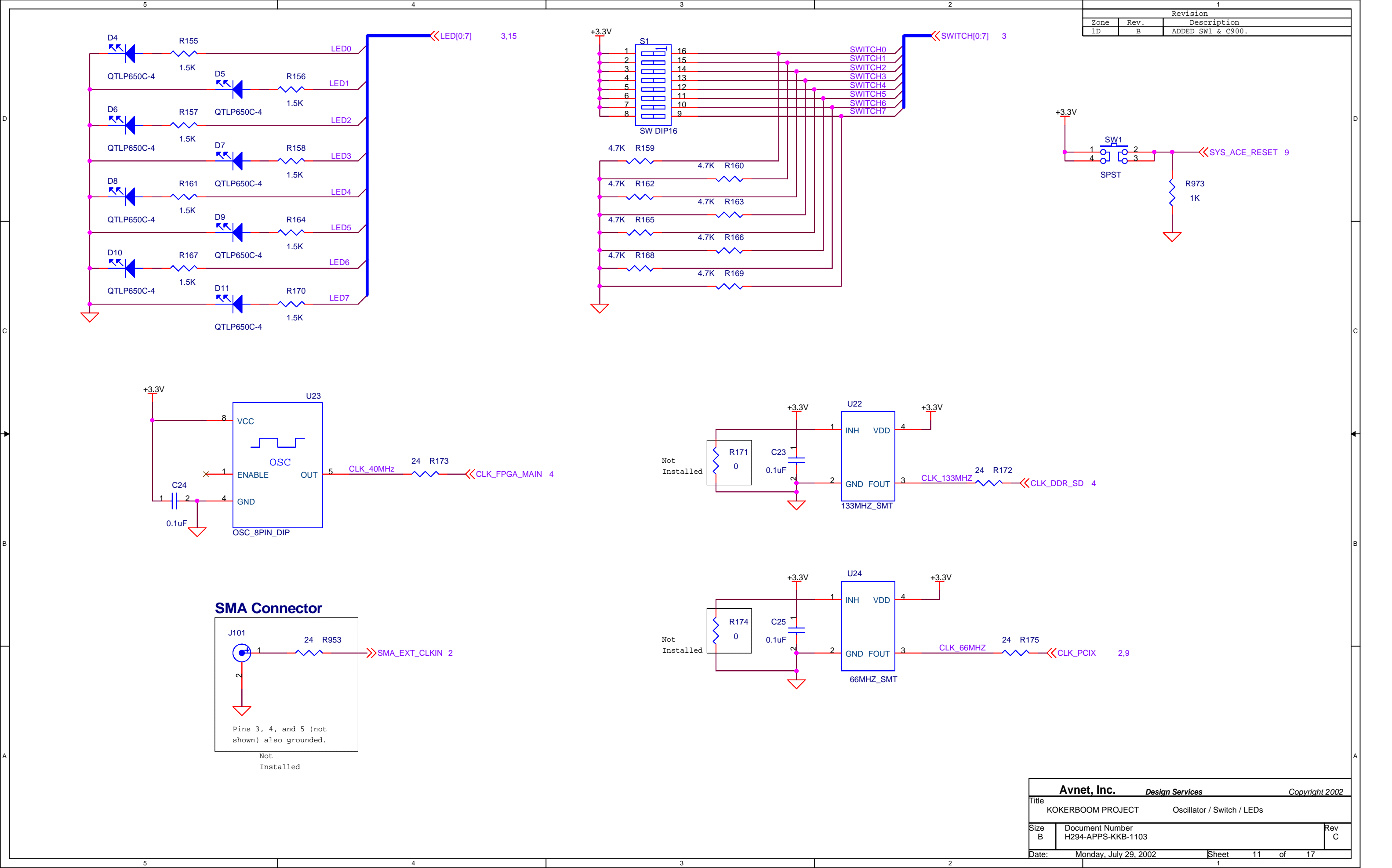
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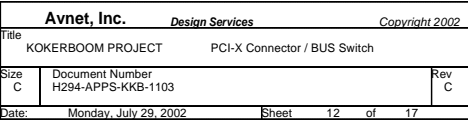




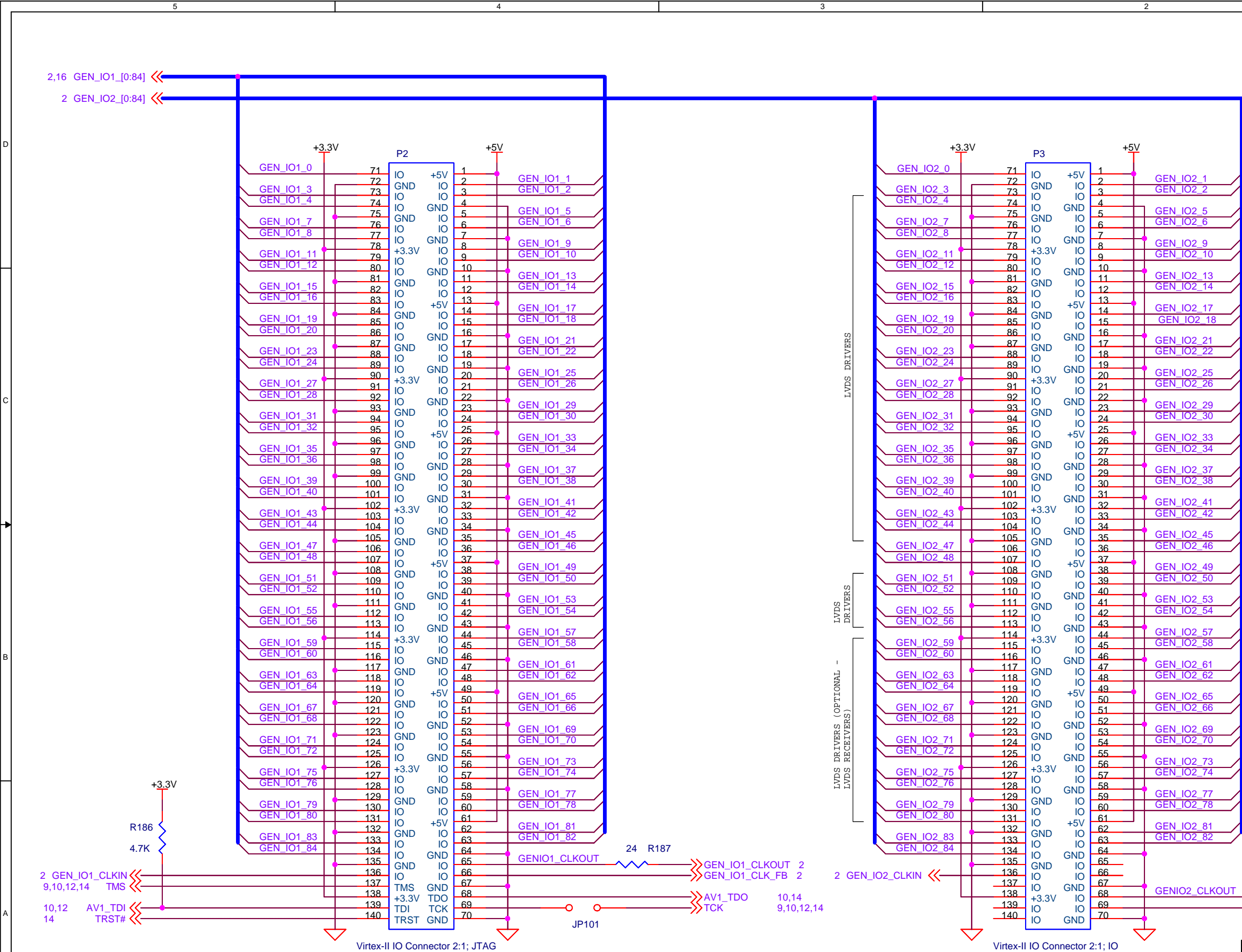


JTAG Chain Selection (Shunts required)		
	JP101	Selection
Pins	2-3	SYSTEM ACE, FPGA & PCIX (bypassed - see PCIX sheet) only
Pins	1-2 & 4-5	Add AvBus Connector 1 only
Pins	1-2 & 5-6	Add AvBus Connector 1 & 2
Pins	1-2, 3-4 & 5-6	Add AvBus Connector 2 only









Revision

Zone	Rev.	Description
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LVDS Pinout Description

RECEIVER (OPT. DRIVER)		DRIVER	
LVDS Signal	GEN_IO2 Signal	LVDS Signal	GEN_IO2 Signal
LVDS_IN1_N	GEN_IO2_1	LVDS_OUT1_N	GEN_IO2_3
LVDS_IN1_P	GEN_IO2_2	LVDS_OUT1_P	GEN_IO2_4
LVDS_IN2_N	GEN_IO2_5	LVDS_OUT2_N	GEN_IO2_7
LVDS_IN2_P	GEN_IO2_6	LVDS_OUT2_P	GEN_IO2_8
LVDS_IN3_N	GEN_IO2_9	LVDS_OUT3_N	GEN_IO2_11
LVDS_IN3_P	GEN_IO2_10	LVDS_OUT3_P	GEN_IO2_12
LVDS_IN4_N	GEN_IO2_13	LVDS_OUT4_N	GEN_IO2_15
LVDS_IN4_P	GEN_IO2_14	LVDS_OUT4_P	GEN_IO2_16
LVDS_IN5_N	GEN_IO2_17	LVDS_OUT5_N	GEN_IO2_19
LVDS_IN5_P	GEN_IO2_18	LVDS_OUT5_P	GEN_IO2_20
LVDS_IN6_N	GEN_IO2_21	LVDS_OUT6_N	GEN_IO2_23
LVDS_IN6_P	GEN_IO2_22	LVDS_OUT6_P	GEN_IO2_24
LVDS_IN7_N	GEN_IO2_25	LVDS_OUT7_N	GEN_IO2_27
LVDS_IN7_P	GEN_IO2_26	LVDS_OUT7_P	GEN_IO2_28
LVDS_IN8_N	GEN_IO2_29	LVDS_OUT8_N	GEN_IO2_31
LVDS_IN8_P	GEN_IO2_30	LVDS_OUT8_P	GEN_IO2_32
LVDS_IN9_N	GEN_IO2_33	LVDS_OUT9_N	GEN_IO2_35
LVDS_IN9_P	GEN_IO2_34	LVDS_OUT9_P	GEN_IO2_36
LVDS_IN10_N	GEN_IO2_37	LVDS_OUT10_N	GEN_IO2_39
LVDS_IN10_P	GEN_IO2_38	LVDS_OUT10_P	GEN_IO2_40
LVDS_IN11_N	GEN_IO2_41	LVDS_OUT11_N	GEN_IO2_43
LVDS_IN11_P	GEN_IO2_42	LVDS_OUT11_P	GEN_IO2_44

LVDS Pinout Description			
DRIVERS (OPT. RECEIVER)		DRIVERS	
LVDS Signal	GEN_IO2 Signal	LVDS Signal	GEN_IO2 Signal
LVDS_OUT12_N	GEN_IO2_65	LVDS_OUT23_N	GEN_IO2_51
LVDS_OUT12_P	GEN_IO2_66	LVDS_OUT23_P	GEN_IO2_52
LVDS_OUT13_N	GEN_IO2_69	LVDS_OUT24_N	GEN_IO2_55
LVDS_OUT13_P	GEN_IO2_70	LVDS_OUT24_P	GEN_IO2_56
LVDS_OUT14_N	GEN_IO2_73	LVDS_OUT25_N	GEN_IO2_53
LVDS_OUT14_P	GEN_IO2_74	LVDS_OUT25_P	GEN_IO2_54
LVDS_OUT15_N	GEN_IO2_77	LVDS_OUT26_N	GEN_IO2_57
LVDS_OUT15_P	GEN_IO2_78	LVDS_OUT26_P	GEN_IO2_58
LVDS_OUT16_N	GEN_IO2_81	LVDS_OUT27_N	GEN_IO2_61
LVDS_OUT16_P	GEN_IO2_82	LVDS_OUT27_P	GEN_IO2_62
LVDS_OUT17_N	GEN_IO2_59		
LVDS_OUT17_P	GEN_IO2_60		
LVDS_OUT18_N	GEN_IO2_63		
LVDS_OUT18_P	GEN_IO2_64		
LVDS_OUT19_N	GEN_IO2_67		
LVDS_OUT19_P	GEN_IO2_68		
LVDS_OUT20_N	GEN_IO2_71		
LVDS_OUT20_P	GEN_IO2_72		
LVDS_OUT21_N	GEN_IO2_75		
LVDS_OUT21_P	GEN_IO2_76		
LVDS_OUT22_N	GEN_IO2_79		
LVDS_OUT22_P	GEN_IO2_80		

Available with larger device

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